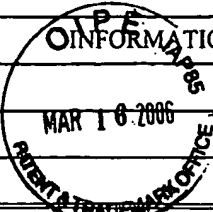
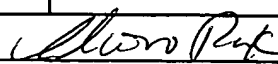


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 INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				BP 3256		10/685,129		
				Applicant(s)				
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U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA							
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	AJ							
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Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL						☐	☐
	AM						☐	☐
	AN						☐	☐
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AR	C.D. Cranor et al., "Architectural Considerations for CPU and Network Interface Integration," http://chuck.cranor.org/p/unum_hoti99.pdf , printed 2/15/05						
	AS	C.D. Cranor et al., "Architectural Considerations for CPU and Network Interface Integration," http://chuck.cranor.org/p/unum_slides_hoti99.pdf , printed 2/15/05						
	AT	C.D. Cranor et al., "Architectural Considerations for CPU and Network Interface Integration," IEEE Micro Chips, Systems, Software, and Applications, Jan/Feb 2000, pp. 18-26						
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